

17/5/22

**K. J. Somaiya Institute of Engineering and Information Technology, Sion, Mumbai-22**  
**(Autonomous College Affiliated to University of Mumbai)**

**End Semester Exam**

**April - May 2022**

**(B.Tech/M.Tech.) Program: Computer Engineering**

**Examination: TY Semester: VI**

**Course Code: IUCEC601 Course Name: System Programming and Compiler Construction**

**Duration: 03 Hours**

**Max. Marks: 60**

**Instructions:**

- (1) All questions are compulsory.
- (2) Draw neat diagrams wherever applicable.
- (3) Assume suitable data, if necessary.

		Max. Marks	CO	BT level
<b>Q 1</b>	<b>Solve any six questions out of eight:</b>	<b>12</b>		
<b>i)</b>	Arrange the following System Softwares according to their role in Program Execution: Linker, Loader, Compiler, Macro-preprocessor, Assembler, Text Editor	<b>2M</b>	CO1	Ap
<b>ii)</b>	Enlist the data structures involved in the design of a Two Pass Assembler.	<b>2M</b>	CO2	R
<b>iii)</b>	Define a Macro with an example	<b>2M</b>	CO3	U
<b>iv)</b>	Enlist the functions of Loader	<b>2M</b>	CO4	R
<b>v)</b>	Define a Lexeme, Token and Pattern with an example	<b>2M</b>	CO5	U

vi)	Enlist the Bottom Up Parsing techniques	2M	CO5	R
vii)	What is the significance of the Code Optimization Phase in the process of Compilation?	2M	CO6	U
viii)	Enlist the different Intermediate Code Representations	2M	CO6	R
<b>Q.2</b>	<b>Solve any four questions out of six.</b>	<b>16M</b>		
i)	Differentiate between System Software & Application Software	4M	CO1	U
ii)	Explain how the Two Pass Assembler resolves Forward referencing problems.	4M	CO2	U
iii)	Explain Nested Macro Call with an example	4M	CO3	U
iv)	Describe absolute loader and compile and go loader	4M	CO4	U
v)	Deduce the FIRST and FOLLOW of the non-terminals in the following Grammar : $S \rightarrow aBDh$ $B \rightarrow cC$ $C \rightarrow bC / \epsilon$ $D \rightarrow g$	4M	CO5	Ap
vi)	Generate the assembly language code for the following Arithmetic Expression : $a = b - (c * d)$ based on code generation algorithm. Also depict the updations in Register descriptors and address descriptors	4M	CO6	Ap
<b>Q.3</b>	<b>Solve any two questions out of three.</b>	<b>16M</b>		
i)	Explain the working of pass 1 macroprocessor with flowchart.	8M	CO3	U
ii)	Construct the Predictive Parsing(LL) table for the given grammar. Also, mention the steps involved in deriving the Parsing Table. $E \rightarrow E + T \mid T$ $T \rightarrow T * F \mid F$ $F \rightarrow (E) \mid id$	8M	CO5	Ap
iii)	Deduce the Operator Precedence Parsing Table for the given grammar. Also, perform the parsing action over the given input string	8M	CO5	Ap

	Grammar : $E \rightarrow E + E \mid E \times E \mid id$ Input string : $id + id * id$			
<b>Q.4</b>	<b>Solve any two questions out of three.</b>	<b>16M</b>		
<b>i)</b>	Using a Flowchart to explain the working of Pass 1 of a Two Pass Assembler.	<b>8M</b>	CO2	U
<b>ii)</b>	Write the Three Address Code (TAC) notation for the following Arithmetic Expression : $p = a + b - (c * d) / e$	<b>8M</b>	CO6	Ap
<b>iii)</b>	Explain any 5 Code Optimization techniques with proper examples.	<b>8M</b>	CO6	U