

(3 Hours)

Total Marks :80

- Note: 1) Question No.1 is compulsory
2) Attempt any Three from the remaining

Q1

- A) Evaluate using Laplace transform $\int_0^t e^{-\sqrt{2}t} \frac{\sin t \sinh t}{t} dt$ 5
 B) Prove that $f(z) = z^n$ is analytic hence find $f'(z)$ 5
 C) Find a Fourier series to represent $f(x) = \sqrt{1 - \cos x}$ in $(-\pi, \pi)$ 5
 D) Find $f(r)$, so that $f(r)\bar{r}$ is solenoidal 5

Q2

- A) Find analytic function $f(z) = u + iv$, if $u = e^x(x \cos y - y \sin y)$ 6
 B) Find the Bilinear transformation which maps the points $z = \infty, i, 0$ onto the points $w = 0, i, \infty$ 6
 C) Obtain the fourier series for $f(x) = \begin{cases} 2\pi - x & , \pi < x < 2\pi \\ x & , 0 < x < \pi \end{cases}$ 8

With period Hence deduce that $\frac{\pi^2}{96} = \frac{1}{1^4} + \frac{1}{3^4} + \frac{1}{5^4} + \dots$

Q3

- A) Find inverse Laplace transform of (i) $\log\left(\frac{s^2+a^2}{s^2+b^2}\right)$ (ii) $\frac{e^{-2s}}{s^2+8s+25}$ 6
 B) Find Complex form of Fourier Series of e^{ax} in $(-a, a)$ 6
 C) Verify Greens Theorem for $\int_C (x^2 - y)dx + (2y^2 + x)dy$ where C is the closed curve of the region bounded by $y = 4$ and $y = x^2$ 8

Q4

- A) Prove that $J_{\frac{1}{2}}(x) = \sqrt{\frac{2}{\pi x}} \cdot \sin x$ 6
 B) Use Gauss's Divergence theorem to evaluate $\iint_S \bar{N} \cdot \bar{F} ds$ where $\bar{F} = x^2i + yzj + yzk$ and S is the surface of the cube bounded by $x=0, x=1, y=0, y=1, z=0, z=1$ 6
 C) Solve using Laplace transform $(D^2 + 2D + 5)y = e^{-t} \sin t$, given $y(0)=0$ and $y'(0)=1$ 8

Q5

- A) Find half range sine series for $f(x) = x(\pi - x)$ in $(0, \pi)$ Hence find value of $\sum \frac{(-1)^n}{(2n-1)^3}$ 6
 B) Find the image of $|z| < 1$ under the bilinear transformation $w = \frac{i-z}{z+i}$ also find the fixed point. 6
 C) Prove that $y = x^{-n} J_n(x)$ is a solution of the equation, $x \frac{d^2 y}{dx^2} + (1+2n) \frac{dy}{dx} + xy = 0$ 8

- Q6
- A) Find the directional derivative of $\phi = x^2 y \cos z$ at $(1, 2, \frac{\pi}{2})$ in the direction of $(2i + 3j + 2k)$ 6
- B) Find inverse Laplace transform of $\frac{1}{(s^2 + 4s + 13)^2}$ using convolution theorem 6
- C) Express the function $f(x) = \begin{cases} -e^{kx} & , x < 0 \\ e^{-kx} & , x > 0 \end{cases}$ as Fourier integral. Hence evaluate $\int_0^\infty \frac{w \sin wx}{w^2 + k^2} dw$ 8

S1302

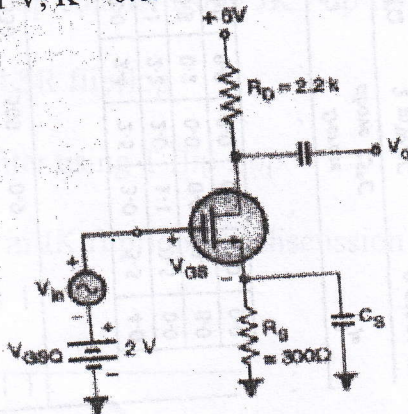
SEM-III - CHOICE BASE - ETRX - 14/05/2019

(3 Hours)

[Total Marks: 80]

- N.B: (1) Question No.1 is compulsory.
 (2) Solves any three out of remaining question.
 (3) Assume suitable data if necessary.

- Q.1 Solve any Four
- a. Draw characteristics of PN junction in thermal equilibrium and explain. 05
 - b. For a BJT amplifier, show with the help of a voltage divider bias circuit, how to draw A.C. load line? Draw graph. 05
 - c. Explain the operation of MOSFET as amplifier. 05
 - d. Explain construction, working principle and characteristics of Photodiode. 05
 - e. Compare HWR, FWR and Bridge rectifier. 05
- Q.2
- a. Draw and explain positive and negative clamper circuit. 10
 - b. Explain common base configuration of BJT as an Amplifier. 10
- Q.3
- a. Draw and explain VI and CV characteristics of P-channel enhancement type MOSFET with symbol. 10
 - b. Explain operation and characteristics of Schottkey diode. 10
- Q.4
- a. Explain the operation of fullwave rectifier and draw the output waveform for V_{Ldc} and I_{Ldc} . 10
 - b. Explain working of BJT considering all possible current density components in an NPN transistor operation in active mode. 10
- Q.5
- a. Design single stage RC coupled amplifier to give a voltage gain of 80 with stability factor better than 11 and output voltage of 3 Vrms. Use NPN transistor with specifications $h_{fe} = 110-800$, $h_{ie} = 4.5 \text{ k}\Omega$, $V_{CE} = 45 \text{ V}$, $I_{c(max)} = 100 \text{ mA}$, $f_L = 300\text{Hz}$, $V_{CC} = 18\text{V}$. 15
 - b. Draw small signal model of PN junction diode. What is the main use of this model? 05
- Q.6
- a. What is the small signal voltage gain of the MOSFET amplifier shown in diagram, if $V_T = 1 \text{ V}$, $K = 0.82\text{mA/V}^2$ and $\lambda = 0.022/\text{V}$. 10



- b. Explain hybrid model of BJT.

DBEC DATA SHEET

Transistor type	P_{max} Watts	I_{cmx} Amps	V_{ce} volts	V_{ce} volts	V_{ce} volts	V_{ce} volts	V_{ce} volts	V_{ce} volts	T_j °C	D.C. current min	current typ.	gain max.	Small min.	Signal typ.	h_{fe} max.	V_{ce} max.	θ_{jc} °C/W	Derate above 25°C W/°C
2N 3055	115.5	15.0	1-1	100	60	70	90	7	200	20	50	70	15	50	120	1.8	1.5	0.7
ECN 055	50.0	5.0	1-0	60	50	55	60	5	200	25	50	100	25	75	125	1.5	3.5	0.4
ECN 149	30.0	4.0	1-0	50	40	—	—	8	150	30	50	110	33	60	115	1.2	4.0	0.3
ECN 100	5.0	0.7	0-6	70	60	65	—	6	200	50	90	280	50	90	280	0.9	35	0.05
BC147A	0.25	0.1	0-25	50	45	50	—	6	125	115	180	220	125	220	260	0.9	—	—
2N 525(PNP)	0.25	0.5	0-25	85	30	—	—	—	100	35	—	65	—	45	—	—	—	—
BC147B	0.25	0.1	0-25	50	45	50	—	6	125	200	290	450	240	330	500	0.9	—	—

Transistor type	h_{ie}	h_{oe}	h_{re}	θ_{ja}
BC 147A	2.7 K Ω	18 μ Ω	1.5 $\times 10^{-4}$	0.4°C/mw
2N 525 (PNP)	1.4 K Ω	25 μ Ω	3.2 $\times 10^{-4}$	—
BC 147B	4.5 K Ω	30 μ Ω	2 $\times 10^{-4}$	0.4°C/mw
ECN 100	50 Ω	—	—	—
ECN 149	15 Ω	—	—	—
ECN 055	12 Ω	—	—	—
2N 3055	6 Ω	—	—	—

BFV 11—JFET MUTUAL CHARACTERISTICS									
-V _{GS} volts	I _{DS} max. mA	I _{DS} typ. mA	I _{DS} min. mA	I _{DS} max. mA	I _{DS} typ. mA	I _{DS} min. mA	I _{DS} max. mA	I _{DS} typ. mA	I _{DS} min. mA
0-0	0-0	0-2	0-4	0-6	0-8	1-0	1-2	1-6	2-0
10	9-0	8-3	7-6	6-8	6-1	5-4	4-2	3-1	2-2
7-0	6-0	5-4	4-6	4-0	3-3	2-7	1-7	0-8	0-2
4-0	3-0	2-2	1-6	1-0	0-5	0-0	0-0	0-0	0-0

(typical)

Derate above 25°C

E / Sem III / Etx / Choice Based / 20-05-2019

Time: 3 Hours

Max Marks:80

- N.B: 1) Question no. 1 is compulsory.
 2) Attempt any three out of the remaining five questions
 3) Use suitable data, wherever necessary.

- Que 1 : Attempt any four questions from the following. (20 M)
- I. Compare TTL and CMOS Logic families with respect to
 - i) Power dissipation ii) Propagation delay iii) Figure of merit iv) Fan-out
 - II. Convert $(73.301)_{10}$ into binary, octal, Hexadecimal and BCD equivalent.
 - III. Simplify following three- variable expression using Boolean algebra
 $\prod M(0, 1, 3, 4, 7)$
 - IV. Design Half adder circuit using basic gate.
 - V. Explain different types of triggering methods used for flipflop
- Que 2 A) Perform following operation: (10 M)
- i) Addition $24_{BCD} + 18_{BCD}$ ii) Subtraction $46_{10} - 22_{10}$ using 2's complement method
- Que 2 B) Reduce the given expression and Realize using NAND gate only. (10 M)
- $$Y = AB' + AC' + C + AD + AB'C + ABC$$
- Que 3 A) Simplify the following function using Quine-Mccluskey method. (10 M)
- $$\sum m(1, 2, 3, 5, 9, 12, 14, 15) + \sum d(4, 8, 11)$$
- Que 3 B) Design 2 Bit Magnitude comparator using gates (10 M)
- Que 4(A) Implement the following Boolean function with 8:1 Multiplexer (10 M)
- $$F(A, B, C, D) = \sum m(0, 2, 6, 10, 11, 12, 13) + \sum d(3, 8, 14)$$
- Que 4(B) Draw and explain working of Bidirectional shift register. (10 M)
- Que 5(A) Design MOD 13 Asynchronous up counter using JK Flip flop (10 M)
- Que 5(B) Convert JK Flip flop into D and SR flipflop (10 M)
- Que 6 (A) Design 3 bit Synchronous counter using T flip flop (10 M)
- Que 6(B) Explain Race around condition in JK flipflop and discuss solution to avoid Race around condition (10 M)

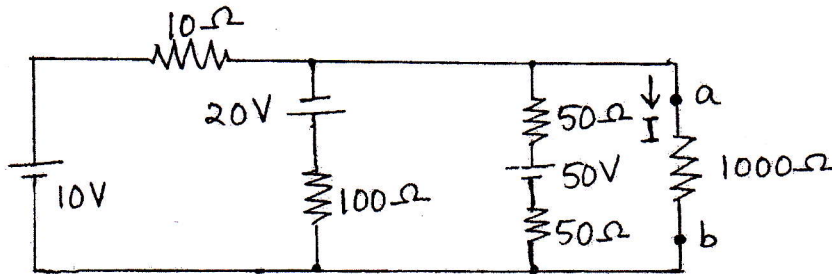
SE (ETRX) Sem III - Choice Based - 24/05/19

(3 Hours)

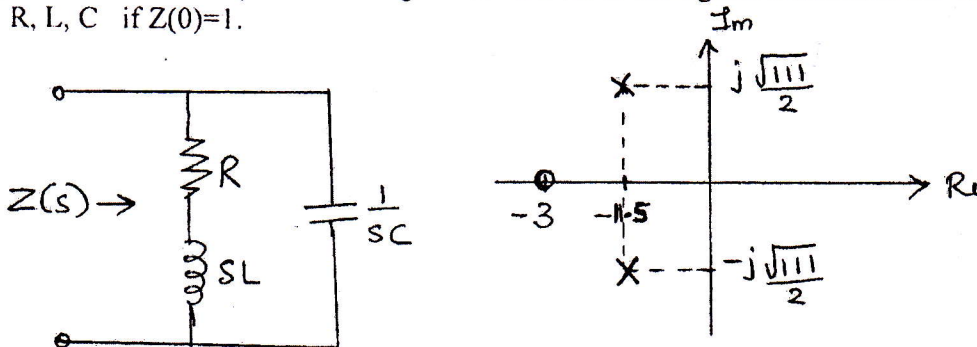
Total Marks: 80

- NB:** (1) Question No. 1 is compulsory.
 (2) Attempt any three questions from the remaining.
 (3) Figures to the right indicate full marks.
 (4) Assume suitable data if required.

1. (a) By constructing Millman's equivalent voltage source at the left of terminals a and b in the given circuit, find the current I . (5)

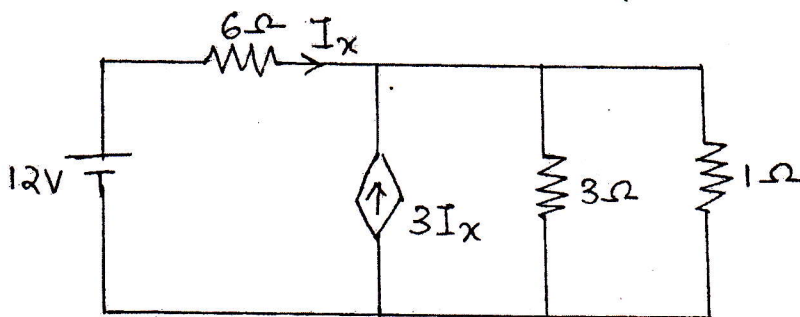


- (b) A network and its pole zero diagram are shown in the figure. Determine the values of R , L , C if $Z(0)=1$. (5)



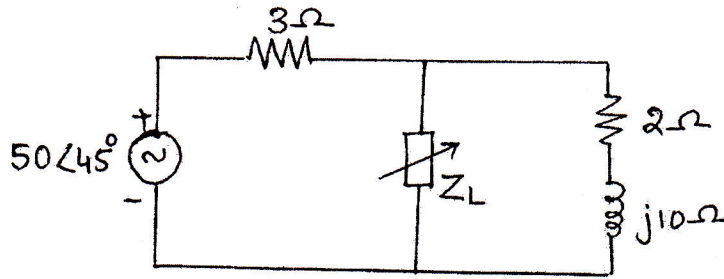
- (c) Obtain Z - parameters in terms of $ABCD$ parameters. (5)
 (d) Explain various types of filters. (5)

2. (a) Find the current through the $1\ \Omega$ resistor in the given network (8)



TURN OVER

- (b) Find the value of load impedance Z_L so that maximum power can be transferred to it in the network of figure. Find maximum power. (6)



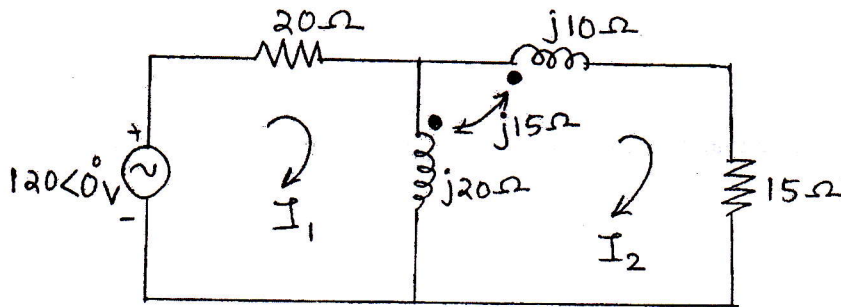
- (c) Design a constant-k low-pass T and π section filters having cut-off frequency of 4kHz and nominal impedance of 500 Ω . (6)

3. (a) Check whether the following polynomials are Hurwitz polynomials: (10)

(i) $F(s) = s^4 + s^3 + 4s^2 + 2s + 3$

(ii) $F(s) = (s+2)^3$

- (b) Find the voltage across the 15 Ω resistor in the given network using mesh analysis. (10)

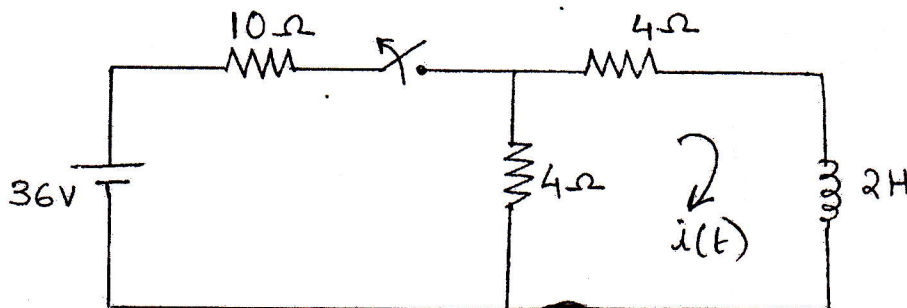


4. (a) Test whether the following functions are positive real functions: (10)

(i) $F(s) = \frac{s^3 + 6s^2 + 7s + 3}{s^2 + 2s + 1}$

(ii) $F(s) = \frac{s(s+3)(s+5)}{(s+1)(s+4)}$

- (b) The network shown in figure has attained steady state with the switch closed for $t < 0$. At $t=0$, the switch is opened. Obtain $i(t)$ for $t > 0$. (10)

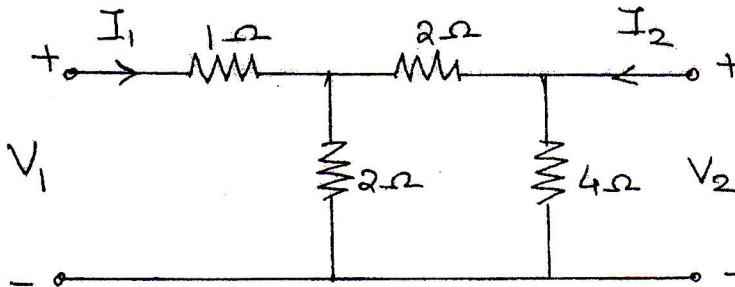


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5. (a) Realize Cauer Form I and Cauer Form II of the following LC impedance function. (8)

$$Z(s) = \frac{(s+1)(s+3)}{s(s+2)}$$

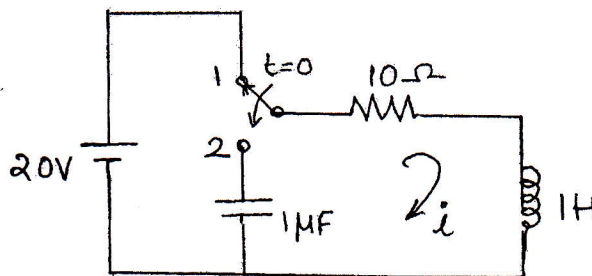
- (b) Determine Y-parameters for the circuit given in figure. (6)



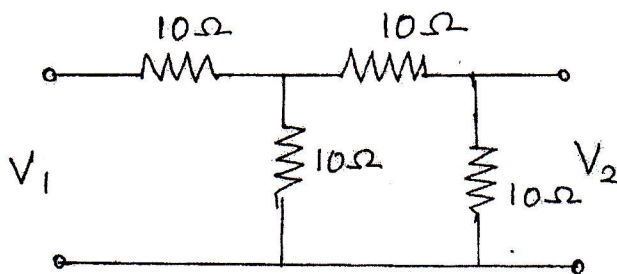
- (c) The voltage $V(s)$ of a network is given by $V(s) = \frac{3s}{(s+2)(s^2+2s+2)}$. Plot its pole-zero diagram and hence obtain $v(t)$. (6)

6. (a) In the circuit given, switch is changed from position 1 to position 2 at time $t=0$.

Find i , $\frac{di}{dt}$ and $\frac{d^2i}{dt^2}$ at time $t=0^+$. (10)



- (b) Find the transmission parameters of the resulting circuit when both are in cascade connection. (10)



SE SEM-III - CHOICE BASE - ETRX - 30/05/2019

Time :- 03 Hours

Max. Marks :- 80

- (i) Question No. 1 is compulsory & attempt any three out of the remaining five questions.
- (ii) Assume suitable data if required but justify it logically wherever applicable.
- (iii) **Figures to the right indicate full marks & every sub-question from Q.2 to Q.6 have equal weightage and have 10 marks each.**

Q.1 ATTEMPT ANY FOUR (04) :-

- a) Explain precision and resolution for electronic equipments.
- b) Draw a neat circuit diagram of LCR – Q meter & explain its operating principle.
- c) Explain specifications of dual trace and dual beam CRO.
- d) Describe operating principle of harmonic distortion analyzer with a neat block diagram. 20
- e) With a neat diagram, explain the principle of digital time measurement.

- Q.2** (a) A set of independent current measurements were recorded as 10.03,10.10,10.11,10.08 A .Calculate a)Average current b) Range of error.

- (b) List and discuss operation and applications of Kelvin bridge 20

- Q.3** (a) Draw the block diagram of CRO and explain its operation. State specifications of CRO.

- (b) Explain how Lissajous patterns / figures are used for measurement of an unknown frequency & phase shift using a cathode ray oscilloscope (CRO). 20

- Q.4** a) Draw the circuit diagram and explain the operation of bridge used to measure capacitance.

- b) Explain various features of digital storage oscilloscope. 20

- Q.5** (a) Draw the neat diagram and explain the operation of dual slope type DVM.

- (b) In a food processing unit, a highly acidic solution is stored in a storage tank where its level has to be continuously monitored round the clock. Your supervisor suggests that due to highly acidic nature of the solution, a non-contact transducer should be used for the level measurement. Which transducer will you use for above application? Describe its operation with a neat diagram. 20

- Q.6** (a) Draw the diagram and explain the operation of Rotameter.

- (b) Explain the operation of linear variable differential transformer. What is residual voltage? 20
