DECEMBER-2019 EXAMINATION TIME TABLE

PROGRAMME - S.E. (Computer) (Choice Based)

SEMESTER - IV

Days and Dates	Time	Paper Code	Paper
Wednesday, December 04, 2019	02:30 p.m. to 05:30 p.m.	40501	APPLIED MATHEMATICS - IV
Monday, December 09, 2019	02:30 p.m. to 05:30 p.m.	40502	ANALYSIS OF ALGORITHM
Wednesday, December 11, 2019	02:30 p.m. to 05:30 p.m.	40503	COMPUTER ORGANIZATION AND ARCHIECTURE
Friday, December 13, 2019	02:30 p.m. to 05:30 p.m.	40504	COMPUTER GRAPHICS
Tuesday, December 17, 2019	02:30 p.m. to 05:30 p.m.	40505	OPERATING SYSTEM

D r · V

i n

0

P a

i l (3 hours) Max. Marks: 80

- **N.B.** (1) Question No. 1 is compulsory.
 - (2) Answer any three questions from Q.2 to Q.6.
 - (3) Use of Statistical Tables permitted.
 - (4) Figures to the right indicate full marks.
- Q.1 (a) Find all the basic solutions to the following problem:

Maximise
$$z = x_1 + 3x_2 + 3x_3$$

subject to $x_1 + 2x_2 + 3x_3 = 4$
 $2x_1 + 3x_2 + 5x_3 = 7$
 $x_1, x_2, x_3 \ge 0$

(b) Evaluate
$$\int_{c}^{c} (z-z^2) dz$$
, where c is upper half of the circle $|z|=1$.

(c) Ten individual are chosen at random from a population & heights are found to be 63, 63, 64, 65, 66, 69, 69, 70, 70, 71.inches. Discuss the suggestion that the height of universe is 65 inches.

(d) If
$$A = \begin{bmatrix} 2 & 3 \\ -3 & -4 \end{bmatrix}$$
, find A^{100}

- Q.2 (a) Evaluate $\int_{c} \frac{z+2}{(z-3)(z-4)} dz$, where c is the circle |z|=1
 - (b) An I.Q. test was administered to 5 persons and after they were trained. The results are given below.

Test whether there is any change in I.Q. after the training programme, use 1% LOS.

(c) Solve the following LPP using Simplex Method

Maximise
$$z = 4x_1 + 10x_2$$

subject to $2x_1 + x_2 \le 10$
 $2x_1 + 5x_2 \le 20$
 $2x_1 + 3x_2 \le 18$
 $x_1, x_2 \ge 0$

Q.3 (a) Find the Eigen values and Eigen vectors of the following matrix.

$$A = \begin{bmatrix} 2 & 2 & 1 \\ 1 & 3 & 1 \\ 1 & 2 & 2 \end{bmatrix}$$
 06

[Turn over

- (b) If the height of 500 students is normally distributed with mean 68 inches and standard deviation 4 inches. Find the expected number of students having heights between 65 & 71 inches.
 - Obtain Taylor's and Laurent's expansions of $f(z) = \frac{z^2 1}{z^2 + 5z + 6}$ around z = 0
- Q.4 (a) A machine is claimed to produce nails of mean length 5 cms & standard of 0.45 cm. A random sample of 100 nails gave 5.1 as their average length. Does the performance of the machine justify the claim? Mention the level of significance you apply.
 - (b) Using the Residue theorem, Evaluate $\int_{0}^{2\pi} \frac{d\theta}{5 + 3\sin\theta}$
 - (c) (i) In a certain manufacturing process 5% of the tools produced turnout to be defective. Find the probability that in a sample of 40 tools at most 2 will be defective.
 - (ii) A random variable x has the probability distribution $P(X = x_i) = \frac{1}{6} {}^{3}C_{X}, X = 0,1,2,3.$ Find the moment generating function of x
- **Q.5** (a) Check whether the following matrix is Derogatory or Non-Derogatory:

$$A = \begin{bmatrix} 6 & -2 & 2 \\ -2 & 3 & -1 \\ 2 & -1 & 3 \end{bmatrix}$$

(b) In an industry 200 workers employed for a specific job were classified according to their performance & training received to test independence of training received & performance. The data are summarized as follows.

Performance	Good	Not good	Total
Trained	100	50	150
Untrained	20	- 30	50
Total	120	80	200

Use χ^2 -test for independence at 5% level of significance & write your conclusion.

(c) Use the dual simplex method to solve the following L.P.P.

Minimise
$$z = 2x_1 + x_2$$

subject to $3x_1 + x_2 \ge 3$
 $4x_1 + 3x_2 \ge 6$
 $x_1 + 2x_2 \le 3$
 $x_1, x_2 \ge 0$

Turn over

04+04

06

Paper / Subject Code: 40501 / Applied Mathematics-IV

Q.6 (a) Show that the matrix A satisfies Cayley-Hamilton theorem and hence find A^{-1} .

Where $A = \begin{bmatrix} 1 & 3 & 7 \\ 4 & 2 & 3 \\ 1 & 2 & 1 \end{bmatrix}$

06

(b) A discrete random variable has the probability density function given below

$X = x_i$	-2	-1	0	1	2	3
$P(x_i)$	0.2	K	0.1	2K	0.1	2K

06

Find K, Mean, Variance.

(c) Using Kuhn-Tucker conditions, solve the following NLPP

Maximise
$$z = 2x_1^2 - 7x_2^2 + 12x_1x_2$$

subject to $2x_1 + 5x_2 \le 98$
 $x_1, x_2 \ge 0$

08

-----END-----

		(3 Hours) [Total Mark	s : 80]
N.B	:: (1) Question No. 1 is compulsory.	400
	(2) Attempt any three out of the remaining five questions.	770
	(3) Assumptions made should be clearly stated.	322 A
			28 29 S
1.	(a)	Explain recurrences and various methods to solve recurrences.	70 5 07
	(b)	Differentiate between P and NP.	5.0
	(c)	Differentiate between Prims and Kruskals algorithm.	5
	(d)	Explain Dynamic programming with example.	5
2.	(a)	Define Branch and Bound and Explain 15 Puzzle problem.	10
_,		Apply dijkstra's algorithm on the following graph.	10
	(0)	Consider vertex 0 as source.	
		8 7 7	9
		10 XXXX	
		7 6 3 5	
3.	(a)	Find Longest Common Subsequence for Following strings:	10
	()	X = ababcde	
		Y = bacadb	
	(b)	Explain Backtracking with n-queen problem.	10
	()	**************************************	
4.	(a)	Formulate Knapsack problem, Explain and differentiate between greedy knapsack	10
		and 0/1 knapsack.	
	(b)	Explain Multistage graph with example.	10
_	(a) I		10
5.	1,50	Rewrite KMP algorithm and explain with example. Define chromatic number of graph. Explain Graph coloring algorithm.	10 10
A	נ (ט)	Define chromatic number of graph. Explain Graph coloring algorithm.	10
6.		Write a short note on following (any 4):	20
		a) Master theorem	
3		b) Rabin Karp algorithm	
533		c) Steps for NP Completeness proofs	
300		d) Assembly line scheduling problem	
7,45	200	e) Strassen's matrix multiplication	

78093 Page **1** of **1**

(3 Hours)

[Total Marks: 80]

Q.1 ((((Answer any four questions Describe the memory hierarchy in the computer system Give different instruction formats. Explain principle of locality of reference in detail Differentiate between Memory Mapped IO and IO Mapped IO. Explain Superscalar Architecture. A program having 10 instructions (without Branch and Call instructions) is executed on non-pipeline and pipeline processors. All instructions are of same length and having 4 pipeline stages and time required to each stage is 1nsec.
(((Describe the memory hierarchy in the computer system Give different instruction formats. Explain principle of locality of reference in detail Differentiate between Memory Mapped IO and IO Mapped IO. Explain Superscalar Architecture. A program having 10 instructions (without Branch and Call instructions) is executed on non-pipeline and pipeline processors. All instructions are of same length and
(Give different instruction formats. Explain principle of locality of reference in detail Differentiate between Memory Mapped IO and IO Mapped IO. Explain Superscalar Architecture. A program having 10 instructions (without Branch and Call instructions) is executed on non-pipeline and pipeline processors. All instructions are of same length and
(Explain principle of locality of reference in detail Differentiate between Memory Mapped IO and IO Mapped IO. Explain Superscalar Architecture. A program having 10 instructions (without Branch and Call instructions) is executed on non-pipeline and pipeline processors. All instructions are of same length and
(Differentiate between Memory Mapped IO and IO Mapped IO. Explain Superscalar Architecture. A program having 10 instructions (without Branch and Call instructions) is executed on non-pipeline and pipeline processors. All instructions are of same length and
(Explain Superscalar Architecture. A program having 10 instructions (without Branch and Call instructions) is executed on non-pipeline and pipeline processors. All instructions are of same length and
	A program having 10 instructions (without Branch and Call instructions) is executed on non-pipeline and pipeline processors. All instructions are of same length and
Q.2 (on non-pipeline and pipeline processors. All instructions are of same length and
	having 4 nineline stages and time required to each stage is 1 nsec
	naving 4 pipeline stages and time required to each stage is 115cc.
	i. Calculate time required to execute the program on Non-pipeline and
	Pipeline processor.
	ii. Calculate Speedup.
() With a neat diagram, explain branch prediction in detail.
B	
Q.3.	Explain page address translation with respect to virtual memory and further explain
	TLB in detail.
) What is "Microprogram"? Write microprogram for following operations.
	i. ADD R1, M, Register R1 and Memory location M are added and result store
	at Register R1.
	ii. MUL R1, R2 Register R1 and Register R2 are multiplied and result store at
	Register R1.
Q.4 (Explain Bus Contention and different method to resolve it. Define instruction pipelining and its various hazards in detail.

Page 1 of 2

71625

Paper / Subject Code: 40503 / Computer Organization and Architecture

Q.5.	(a)	Explain multi core processor architecture in detail	
	(b)	Explain Booth's Multiplication algorithm and Perform (17) ₁₀ X (-5)10. [10]
Q.6		Write short notes on any two	[20]
	(a)	Data transfer techniques	
	(b)	Set associative cache mapping	
	(c)	Flynn's Classification	
	(d)	Control unit of processor	

71625 Page 2 of 2

[Time: Three Hours] [Marks: 80]

Please check whether you have got the right question paper.

N.B:	(1)	Question	No.1	is	com	pulso	rv
11.2.	(- /	Z acouton	110.1	10	00111		• J

- (2) Attempt any three of remaining five questions
- (3) Assume any suitable data if necessary and justify the same

Q 1	a)	Define the following terms: Resolution, Aspect Ratio, Phosphorescence and Fluorescence.	05
	b)	What is the purpose of Inside-Outside Test, explain any one method.	05
	c)	Draw the diagram of CRT and explain its working.	05
	d)	What do you understand by Control points, Degree of Continuity, Local and	05
	/	Global control w.r.t. Curve Generation?	
Q 2	a)	Explain DDA Line drawing algorithm and Plot the points for line AB (A (10, 15)	10
		B (5,25) using it.	
	b)	Explain Area subdivision algorithm for hidden surface removal.	10
Q 3	a)	What is aliasing, how it affects the appearance of an object. Explain any two Anti-	10
		aliasing methods.	
	b)	Explain Liang Barsky line clipping algorithm, what is its benefit over Cohen Sutherland algorithm? Clip the line with co-ordinates $(5, 10)$ and $(35, 30)$ against the window $(x_{min}, y_{min}) = (10, 10)$ and $(x_{max}, y_{max}) = (20, 20)$.	10
Q 4	a)	What is shading? Explain Gouraud and Phong Shading with their pros and cons.	10
	b)	Explain what is meant by B Spline curve? State the various properties of B Spline curve.	10
Q 5	a)	Explain Scan line polygon fill algorithm with the help of suitable diagrams.	10
71/1/2/2 73/2/2/2	b)	Explain the steps for 2D reflection w.r.t. line y=mx and also derive a composite transformation Matrix.	10
Q 6	200	Write short notes on (any two)	20
66	a)	Fractals	
XX AX	b)	Sweep Representation and CSG Method	
EVY VY	c)	Bezier curve and the properties	
	d)	Halftone and Dithering	
V / / -	Y ~() .		

76033 Page **1** of **1**

Time: 3 Hours

Marks: 80

1. Question 1 is compulsory. 2. Attempt any **three** from remaining five questions. 3. Assume suitable data where required. 1 a. Discuss Operating System as a Resource Manager. [5] b. Draw process state diagram and explain the following [5] states: [5] 1. New [5] 2. Ready 3. Running 4. Wait 5. Suspended ready 6. Suspended wait c. Describe Microkernel with a diagram. d. Discuss the importance of "Multithreading". Differentiate between kernel and user thread. 2 a. Differentiate between short term, medium term and long [10] term scheduler with a diagram. b. Calculate AWT, ATAT, Response Time and Throughput of [10] the following processes using Shortest job first (Non Preemptive). Arrival Time **Process Burst Time** (ms) (ms) **P**1 1 7 P2 2 5 3 1 **P**3 2 P4 4 **P5** 5 8 a. What are Semaphores? Differentiate between Counting and 3 [10] Binary Semaphores. Discuss Dinning Philosopher problem. b. What do you understand by a deadlock? Explain deadlock avoidance method. [10] 4 a. Explain different types of memory fragmentation. [8] b. Compare the performance of FIFO, LRU and Optimal based [12] on number of page hit for the following string. Frame size = 3; String (pages): 1 2 3 4 5 2 1 3 3 2 4 5

Paper / Subject Code: 40505 / Operating System

3	a.	Interrupt driven IO over programmed IO.	(10)
	b.	Discuss various disk scheduling methods.	[10]
6.	a.	Discuss various File Allocation Mechanism and their advantages.	[10]
	b.	Explain Unix iNode Structure in detail.	[10]

58567 Page **2** of **2**